

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	132	register same (switch switches) same memory same buffer and impedance and @ad<"19940404"	USPAT; USOCR	OR	OFF	2005/05/25 14:57
L2	24	register same (switch switches) same memory same buffer and impedance and @ad<"19940404" and test adj mode	USPAT; USOCR	OR	OFF	2005/05/25 15:20
L3	1	("6,694,465").PN.	USPAT; USOCR	OR	OFF	2005/05/25 15:20